

28/1



Docket No.: M4065.0904/P904
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Chandra Mouli

Application No.: 10/602,716

Confirmation No.: 9955

Filed: June 25, 2003

Art Unit: 2811

For: TAILORING GATE WORK-FUNCTION
IN IMAGE SENSORS

Examiner: Not Yet Assigned

INFORMATION DISCLOSURE STATEMENT (IDS)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits as far as is known to the undersigned (37 CFR 1.97(b)(3)).

A copy of each reference on PTO/SB/08 is attached.

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists. In accordance with 37 CFR 1.97(h), the filing of this Information Disclosure statement shall not be construed to be an

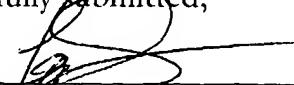
admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

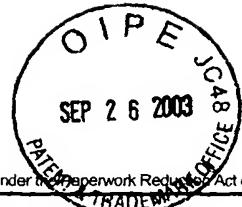
It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M4065.0904/P904.

Dated: September 26, 2003

Respectfully submitted,

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PTO/SB/08a/b (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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Substitute for form 1449A/B/PTO				Complete if Known	
				Application Number	10/602,716-Conf. #9955
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				First Named Inventor	Chandra Mouli
				Art Unit	2811
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	M4065.0904/P904

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)	MM-DD-YYYY		

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	MM-DD-YYYY		

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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
	CA	Nadia Lifshitz – “Dependence of the Work-Function Difference Between the Polysilicon Gate and Silicon Substrate on the Doping Level in Polysilicon,” IEEE Transactions on Electron Devices, Vol. Ed-32, No. 3, March 1985.			T ²
	CB	Tsu-Jae King et al. – “Electrical Properties of Heavily Doped Polycrystalline Silicon-Germanium Films,” IEEE Transactions on Electron Devices, Vol. 41, No. 2, February 1994.			
	CC	Sunetra Mendis et al. – “CMOS Active Pixel Image Sensor,” IEEE Transactions on Electron Devices, Vol. 41, No. 3, March 1994.			
	CD	R. H. Nixon et al. – “256 x 256 CMOS Active Pixel Sensor Camera-on-a-Chip,” IEEE Journal of Solid-State Circuits, Vol. 31, No. 12, December 1996.			

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